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CONFIRMATION NO. 9069

Bib Data Sheet

SERIAL NUMBER 10/620,492	FILING DATE 07/16/2003 RULE	CLASS 438	GROUP ART UNIT 2812	ATTORNEY DOCKET NO. TI-35375
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APPLICANTS

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** CONTINUING DATA
None WLL

** FOREIGN APPLICATIONS
None WLL

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
 ** 10/15/2003

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>WLL</i> Examiner's Signature Initials	STATE OR COUNTRY TX	SHEETS DRAWING 17	TOTAL CLAIMS 44	INDEPENDENT CLAIMS 4
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ADDRESS
 23494
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TITLE
 Method to reduce transistor gate to source/drain overlap capacitance by incorporation of carbon

FILING FEE RECEIVED	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees
		<input type="checkbox"/> 1.16 Fees (Filing)
		<input type="checkbox"/> 1.17 Fees (Processing Ext. of time)
		<input type="checkbox"/> 1.18 Fees (Issue)